

KESHAV MAHAVIDYALAYA
(University of Delhi)

Faculty Time Table

Faculty Name: Dr. Jyoti Bansal					Department: Electronics			Session: 2026-27 (Odd Sem)		
Period	I	II	III	IV	1:0 0 PM	V	VI	VII	VIII	
Days/Time	9:00 AM	10:00 AM	11.00 AM	12.00 Noon		1:30 PM	2:30 PM	3:30 PM	4:30 PM	
Monday	B.Sc(H) Electronics VII Advanced Machine Leaning (#303)	B.Sc(H) Electronics VII Advanced Machine Leaning (#303)	B.Sc(H) Electronics V Embedded Systems Practicals (#306)		L U N C H B R E A K					
Tuesday	B.Sc(H) Electronics VII Advanced Machine Leaning (#303)	B.Sc(H) Electronics I Semiconductor Devices (#304)								
Wednesday	B.Sc(H) Electronics I Semiconductor Devices (#304)	B.Sc(H) Electronics I Semiconductor Devices (#304)	B.Sc(H) Electronics VII Control Systems Practicals (#303)							
Thursday	B.Sc(H) Electronics VII Advanced Machine Leaning Practicals (#303)		B.Sc(H) Electronics V Basic VLSI Practicals (#303)							
Friday			B.Sc(H) Electronics I Circuit Theory and Network Analysis Practicals (#306))				B.Sc(H) Electronics I Semiconductor Devices Practicals (#306)			

Details of Workload

S. No.	Course & Semester	Paper Name (with Paper Code)	Lect./Prac./Tut.	Number of Periods/Week
1	B.Sc(H) Electronics VII	Advanced Machine Leaning (2513010017)	3/2/0	5
2	B.Sc(H) Electronics I	Semiconductor Devices(2512011103)	3/2/0	5
3	B.Sc(H) Electronics VII	Control Systems (2512014701)	0/1/0	1
4	B.Sc(H) Electronics I	Circuit Theory and Network Analysis (2512011102)	0/2/0	2
5	B.Sc(H) Electronics V	Embedded Systems (2512013501)	0/1/0	1
6	B.Sc(H) Electronics V	Basic VLSI Design (2512013503)	0/2/0	2
Convener/Member Departmental Time-Table Committee		Teacher In-Charge/Course Coordinator	Convener/Member Central Time-Table Committee	Principal